



**EXAMINATION BRANCH**

**TIME-TABLE**

**TIME: 10.00 A.M TO 01.00 P.M**

**M.TECH. I SEM. (RA18 REGULATION – 2018 & 2019 BATCHES) REGULAR EXAMINATIONS, JANUARY-2020**

Date & Day	POWER ELECTRONICS	EMBEDDED SYSTEMS	ELECTRONICS DESIGN TECHNOLOGY	ADVANCED MANUFACTURING SYSTEMS	COMPUTER SCIENCE & ENGINEERING
<b>27.01.2020 MONDAY</b>	Special Machines	Digital System Design	Digital System Design	Computer Integrated Manufacturing	Advanced Data Structures
<b>29.01.2020 WEDNESDAY</b>	Analysis of Power Electronic Converters	RTL Simulation and Synthesis with PLDs	RTL Simulation and Synthesis with PLDs	Advanced Computer Aided Design & Manufacturing	Machine Learning
<b>31.01.2020 FRIDAY</b>	Research Methodology and IPR	Research Methodology and IPR	Research Methodology and IPR	Research Methodology and IPR	Research Methodology and IPR
<b>03.02.2020 MONDAY</b>	Power Electronic Control of DC Drives	--	Analog Integrated Circuit Design	Advanced Material Technology	Distributed Systems
<b>05.02.2020 WEDNESDAY</b>	--	Embedded C	Embedded C	--	Software Process & Project Management
<b>07.02.2020 FRIDAY</b>	HVDC Transmission	ARM Architectures	--	Special Manufacturing Process	--

NOTE: ANY CLASHES / OMISSIONS IN THIS TIME-TABLE MAY BE BROUGHT TO THE NOTICE OF THE UNDERSIGNED.

**ADDL. CONTROLLER OF EXAMINATIONS**

Date: 31.12.2019

**CONTROLLER OF EXAMINATIONS**



**EXAMINATION BRANCH**

TIME-TABLE

TIME: 10.00 A.M TO 01.00 P.M

**M.TECH. I SEM. (RA15 REGULATION – 2015, 2016, 2017 BATCHES) SUPPLEMENTARY EXAMINATIONS, JANUARY-2020**

Date & Day	POWER ELECTRONICS	EMBEDDED SYSTEMS	ELECTRONICS DESIGN TECHNOLOGY	ADVANCED MANUFACTURING SYSTEMS	COMPUTER SCIENCE & ENGINEERING	SOFTWARE ENGINEERING
27.01.2020 MONDAY	Machine Modelling & Analysis	<u>Elective – II</u> Digital System Design	<u>Elective – I</u> Digital System Design	<u>Elective – II</u> Precision Engineering	Big data and Analytics	Software Requirements and Estimation
29.01.2020 WEDNESDAY	Analysis of Power Electronic Converters	<u>Elective – I</u> VLSI Technology and Design	VLSI Technology and Design	Advanced Computer Aided Design & Manufacturing	Machine Learning	Web Technologies and Services
31.01.2020 FRIDAY	Modern Control Theory	Real Time Operating System	CMOS Analog Integrated Circuit Design	Theory of Metal Cutting	<u>Elective – II</u> Software Architecture and Design Patterns	<u>Elective – I</u> Information Security and Audit
03.02.2020 MONDAY	Power Electronic Control of DC Drives	Embedded System Design	Embedded System Design	<u>Elective – III</u> Applied Tribology	Distributed Systems	Software Development Methodologies
05.02.2020 WEDNESDAY	<u>Elective – II</u> Dynamics of Electrical Machines	<u>Elective – III</u> TCP/IP Networks	<u>Elective – II</u> CPLD and FPGA Architectures and Applications	Automation in Manufacturing	<u>Elective – I</u> Software Process and Project Management	<u>Elective – II</u> Cyber Security and Cyber Laws
07.02.2020 FRIDAY	<u>Elective – I</u> High Voltage DC Transmission	ARM Architectures	<u>Elective – III</u> Advanced Data Communications	<u>Elective – I</u> Special Manufacturing Process	Advanced Data Structures and Algorithms	Advanced Data Structures and Algorithms

NOTE: ANY CLASHES / OMISSIONS IN THIS TIME-TABLE MAY BE BROUGHT TO THE NOTICE OF THE UNDERSIGNED.

ADDL. CONTROLLER OF EXAMINATIONS

Date: 31.12.2019

CONTROLLER OF EXAMINATIONS



**EXAMINATION BRANCH**

**TIME-TABLE**

**TIME: 10.00 A.M TO 01.00 P.M**

**M.TECH. I SEM. (RA13 REGULATION – 2014, 2013 BATCHES ONLY) SUPPLEMENTARY EXAMINATIONS, JANUARY-2020**

Date & Day	POWER ELECTRONICS	POWER ENGG & ENERGY SYSTEMS	EMBEDDED SYSTEMS	ELECTRONICS DESIGN TECHNOLOGY	ADVANCED MANUFACTURING SYSTEMS	COMPUTER SCIENCE & ENGINEERING	SOFTWARE ENGINEERING
27.01.2020 MONDAY	Machine Modelling & Analysis	Advanced Power System Analysis	<u>Elective – II</u> Digital System Design	<u>Elective – I</u> Digital System Design	Precision Engineering	<u>Elective – II</u> Parallel and Distributed Algorithms	Software Requirements and Estimation
						Advanced Problem Solving	
29.01.2020 WEDNESDAY	Analysis of Power Electronic Converters	Advanced Power System Protection	<u>Elective – I</u> VLSI Technology and Design	VLSI Technology and Design	<u>Elective – II</u> Advanced CAD	Computer System Design	Web Technologies and Services
31.01.2020 FRIDAY	Modern Control Theory	Modern Control Theory	Embedded Real Time Operating System	CMOS Analog Integrated Circuit Design	Design for Manufacturing and Assembly	Advanced Operating Systems	<u>Elective – I</u> Information Security and Audit
						Embedded Systems	Storage Area Networks
03.02.2020 MONDAY	Power Electronic Control of DC Drives	Renewable Energy Systems	Embedded System Design	<u>Elective – II</u> CMOS Digital Integrated Circuit Design	Materials Technology	Distributed Systems	Software Development Methodologies
						Java and Web Technologies	Java and Web Technologies
05.02.2020 WEDNESDAY	<u>Elective – II</u> Dynamics of Electrical Machines	<u>Elective – I</u> High Voltage Engineering	Embedded C	CPLD and FPGA Architectures and Applications	Automation in Manufacturing	<u>Elective – I</u> Software Process and Project Management	Software Process and Project Management
						Object Oriented Modelling	<u>Elective – II</u> Object Oriented Modelling
07.02.2020 FRIDAY	<u>Elective – I</u> High Voltage DC Transmission	<u>Elective – II</u> Power quality	Microcontrollers for Embedded System Design	Microcontrollers for Embedded System Design	<u>Elective – I</u> Special Manufacturing Process	Advanced Data Structures & Algorithms	Advanced Data Structures & Algorithms
		High Voltage DC Transmission				Advanced Compiler Design	Advanced Computer Networks

NOTE: ANY CLASHES / OMISSIONS IN THIS TIME-TABLE MAY BE BROUGHT TO THE NOTICE OF THE UNDERSIGNED.

  
ADDL. CONTROLLER OF EXAMINATIONS

Date: 31.12.2019

  
CONTROLLER OF EXAMINATIONS

**EXAMINATION BRANCH**

**TIME-TABLE**

**TIME: 10.00 A.M TO 01.00 P.M**

**M.TECH. II SEM. (RA18 REGULATION – 2018 BATCH) SUPPLEMENTARY EXAMINATIONS, JANUARY-2020**

Date & Day	POWER ELECTRONICS	EMBEDDED SYSTEMS	ELECTRONICS DESIGN TECHNOLOGY	ADVANCED MANUFACTURING SYSTEMS	COMPUTER SCIENCE & ENGINEERING
<b>28.01.2020 TUESDAY</b>	Power Electronic Control of AC Drives	Embedded OS & RTOS	CMOS VLSI Design	Manufacturing Systems Simulation	Advanced Algorithms
<b>30.01.2020 THURSDAY</b>	Power Electronic Applications to Renewable Energy	--	ASIC and SOC	Theory of Metal Cutting and Forming	Big Data Analytics
<b>01.02.2020 SATURDAY</b>	Microprocessor and Microcontroller	Advanced Digital Signal Processing	Advanced Digital Signal Processing	Advanced Tool Design	Deep Learning
<b>04.02.2020 TUESDAY</b>	--	Embedded Networking	Embedded Networking	NDE & NDT	--
<b>06.02.2020 THURSDAY</b>	Power Quality	System On Chip Architecture	--	--	Cryptography and Network Security

NOTE: ANY CLASHES / OMISSIONS IN THIS TIME-TABLE MAY BE BROUGHT TO THE NOTICE OF THE UNDERSIGNED.



**ADDL. CONTROLLER OF EXAMINATIONS**

Date: 31.12.2019



**CONTROLLER OF EXAMINATIONS**





**EXAMINATION BRANCH**

TIME-TABLE

TIME: 10.00 A.M TO 01.00 P.M

**M.TECH. II SEM. (RA15 REGULATION – 2015, 2016, 2017 BATCHES) SUPPLEMENTARY EXAMINATIONS, JANUARY-2020**

Date & Day	POWER ELECTRONICS	EMBEDDED SYSTEMS	ELECTRONICS DESIGN TECHNOLOGY	ADVANCED MANUFACTURING SYSTEMS	COMPUTER SCIENCE & ENGINEERING	SOFTWARE ENGINEERING
<b>28.01.2020</b> <b>TUESDAY</b>	Power Electronic Control of AC Drives	CPLD and FPGA Architectures and Applications	Real Time Operating Systems	Manufacturing Systems Simulation Modelling & Analysis	Advanced Network Programming	Software Process and Project Management
<b>30.01.2020</b> <b>THURSDAY</b>	Neural And Fuzzy Systems	Hardware Software Co-Design Network Security and Cryptography	CMOS Mixed Signal Circuit Design	Quality Engineering in Manufacturing	High Performance Computing	Cloud Computing
<b>01.02.2020</b> <b>SATURDAY</b>	Microprocessor and Microcontroller	Digital Signal Processors and Architectures	Embedded C Digital Signal Processors and Architectures	Materials Technology	Adhoc and Sensor Networks Database Security	Scripting Languages
<b>04.02.2020</b> <b>TUESDAY</b>	Flexible AC Transmission Systems (FACTS)	Embedded Networking	Embedded Networking	Production & Operations Management	Web Services and Service Oriented Architecture	Big Data and Analytics
<b>06.02.2020</b> <b>THURSDAY</b>	Power Quality	System On Chip Architecture Sensors and Actuators	Ad-hoc Wireless and Sensor Networks Sensors and Actuators	Concurrent Engineering & Product Life Cycle Management	Software Quality Assurance & Testing	Software Quality Assurance and Testing
<b>08.02.2020</b> <b>SATURDAY</b>	Reliability Engineering	Device Modelling Wireless LANs and PANs	Low Power VLSI Design	Design and Manufacturing of MEMS & Micro Systems	Number theory and Cryptography	Software Architecture and Design Patterns

NOTE: ANY CLASHES / OMISSIONS IN THIS TIME-TABLE MAY BE BROUGHT TO THE NOTICE OF THE UNDERSIGNED.

**ADDL. CONTROLLER OF EXAMINATIONS**

Date: 31.12.2019

**CONTROLLER OF EXAMINATIONS**

**EXAMINATION BRANCH**

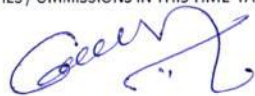
TIME-TABLE

TIME: 10.00 A.M TO 01.00 P.M

**M.TECH. II SEM. (RA13 REGULATION – 2014, 2013 BATCHES ONLY) SUPPLEMENTARY EXAMINATIONS, JANUARY-2020**

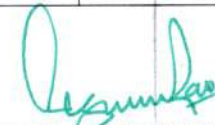
Date & Day	POWER ELECTRONICS	POWER ENGINEERING & ENERGY SYSTEMS	EMBEDDED SYSTEMS	ELECTRONICS DESIGN TECHNOLOGY	ADVANCED MANUFACTURING SYSTEMS	COMPUTER SCIENCE & ENGINEERING	SOFTWARE ENGINEERING
28.01.2020 TUESDAY	Power Electronic Control of AC Drives	Solar Thermal And PV System	CPLD and FPGA Architectures and Applications	Embedded Real Time Operating Systems	Simulation Modeling of Manufacturing Systems	Advanced Network Programming	Software Process and Project Management
						Distributed Computing	Distributed Computing
30.01.2020 THURSDAY	Neural And Fuzzy Systems	Wind Energy System	Hardware Software Co-Design	CMOS Mixed Signal Circuit Design	Quality Engineering in Manufacturing	Advanced Databases	<u>Elective -IV</u> Cloud Computing
						Distributed Databases	Image Processing & Pattern Recognition
01.02.2020 SATURDAY	Microprocessor and Microcontroller	Power System Operation and Deregulation	Digital Signal Processors and Architectures	<u>Elective -I</u> Digital Signal Processors and Architectures	Computer Aided Manufacturing	<u>Elective – III</u> Advanced Data Mining	<u>Elective – III</u> Information Retrieval Systems
						Semantic Web and Social Networks	Semantic Web and Social Networks
04.02.2020 TUESDAY	Flexible AC Transmission Systems (FACTS)	Flexible AC Transmission Systems (FACTS)	Embedded Networking	Embedded C	Production and Operations Management	Web Services and Service Oriented Architecture	Component Based Software Engineering
						Advanced Computer Architecture	Service oriented architecture
06.02.2020 THURSDAY	<u>Elective -III</u> Power Quality	<u>Elective -III</u> Gas Insulated Systems (GIS)	<u>Elective -III</u> Wireless Communications and Networks	<u>Elective -II</u> Low Power VLSI Design	<u>Elective -IV</u> Intelligent Manufacturing Systems	<u>Elective -IV</u> Wireless Networks & Mobile Computing	Software quality assurance and testing
			Sensors and Actuators	Sensors and Actuators		Information Security	
08.02.2020 SATURDAY	<u>Elective -IV</u> Reliability Engineering	<u>Elective -IV</u> Voltage stability	<u>Elective -IV</u> Wireless LANs and PANs	Design for Testability	<u>Elective – III</u> Tool Design	Grid and Cloud Computing	Software architecture and design patterns
			System On Chip Architecture			Advanced Computer Networks	

NOTE: ANY CLASHES / OMISSIONS IN THIS TIME-TABLE MAY BE BROUGHT TO THE NOTICE OF THE UNDERSIGNED.



ADDL. CONTROLLER OF EXAMINATIONS

Date: 31.12.2019



CONTROLLER OF EXAMINATIONS